Topology Selection for Energy Minimization in Embedded Networks

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ABSTRACT
The trend towards distributed, networked embedded systems is changing the way power should be managed. Power consumed by bus and network interfaces now matches if not surpasses that of the CPU and is thus becoming a prime candidate for reduction. This paper explores the energy-efficient bus topologies as a new technique for global power optimization of embedded systems that are interconnected by high-speed serial network-like busses such as FireWire and a new generation of SoC busses. Our grammar-based representation for these networks enables the modeling and facilitates selection of energy-efficient bus topology. Experimental results show 15-20% energy saving on the network interfaces without sacrificing system performance.

1. INTRODUCTION
A recent trend in power-aware designs is communication centric power management. In both embedded systems and system-on-chip (SoC) architectures, much of the research work in the past decade has gone into making the CPU very power efficient, and the CPU is now consuming a much smaller fraction of the system power. At the same time, bus and network interfaces are consuming the same if not more power. Higher level integration helps alleviate the situation somewhat, but even processors with built-in network interfaces often require two supply voltages: a lower voltage for the core, and a higher voltage for the off-chip I/O. System-on-chip architectures will also face similar issues, as IP components are increasingly being integrated using on-chip networks for power and modularity advantages.

Communication-centric power management schemes can be divided into custom protocols vs. standard protocols. Custom protocols that utilize application-specific coding schemes [18, 15, 13, 9, 2], custom bus voltages [14], or custom bus segmentation schemes [21, 12, 6] can potentially achieve much better energy efficiency, but they are applicable mainly to closed systems. Most embedded systems and IP components must be interoperable with existing standards, and this limits the types of optimization possible. This paper does not attempt to propose a new standard to compete against the more established ones [3, 7, 8, 10, 11]; instead, it is intended to demonstrate how an existing standard can incorporate energy efficient optimizations. Some of the most important parameters include communication speed and bus topology. This paper investigates topology selection for FireWire, a hot-pluggable, low-power, high-speed serial bus that can support real-time streaming (isochronous) and asynchronous transfer modes. It is widely available on many embedded systems and computers today.

FireWire bus architecture requires a tree topology. Furthermore, each FireWire component has a limited number of ports and a maximum transfer speed available. Our approach to achieve energy reduction is a grammar-driven, constraint-based searching process for low energy network topology. The advantages are: a) the formal method is a systematic way of modeling and generating topologies; b) our technique is extensible to other buses/networks and is beneficial to system-on-chip design with on-chip networks; c) it is orthogonal to most of the existing CPU-centric power management techniques, thus enabling additive energy savings by combining our techniques with existing ones. Our experimental results show up to 15% to 20% energy savings for network interfaces without sacrificing system performance.

This paper is organized as follows. Section 2 provides FireWire backgrounds and reviews related work. Section 3 presents a formal problem formulation, while Section 4 describes the algorithms we used to search optimal tree topologies. We discuss the experimental results in Section 5. Finally, we conclude our work in Section 6.

2. BACKGROUND AND RELATED WORK

2.1 FireWire Bus
FireWire (IEEE1394) [4] is a high-speed serial bus standard. 1394a currently supports transmission speeds up to 400Mbps, and the new 1394b standard [1] will support transmission speeds of 800Mbps and 1600Mbps. FireWire was designed to connect a computer to peripherals like hard disks, scanners, and consumer electronics like video cameras. It is now widely available on many computers, set-top boxes, and embedded systems in automotive and aerospace domains. FireWire supports two data transfer types: asynchronous and isochronous transfer modes. Asynchronous mode guarantees the data delivery with acknowledgment. Isochronous mode guarantees data bandwidth without acknowledgment, and it is suitable for real-time streams such as like video.

FireWire is hot-pluggable and can connect up to 63 devices. 1394a cables can run as long as 4.5 meters, and packets can take up to 16
hops for a maximum total distance of 72 meters. Future standard extends the single hop distance to up to 100 meters and use fiber optics as physical media. When a new node is attached to the bus, or an existing node is unplugged, the bus will go through bus reset. First, a root will be elected, followed by tree identification and self-identification processes, after which the new topology map and speed map is broadcast to every node. Unlike the Universal Serial Bus (USB), which is host-based, FireWire is peer-to-peer.

FireWire imposes a number of restrictions. First, the network must be acyclic. This implies that there is a unique path between any pair of communicating nodes. Second, all intermediate nodes on the path must be powered on (at least the physical layer controller) to act as repeaters. Third, all the intermediate nodes must support the transfer speed of the communicating nodes, otherwise the transaction cannot be started. Fourth, the fan-out of each node is constrained by the number of ports available on the physical interface.

2.2 Power Management with FireWire

Power management opportunities with a standard protocol like FireWire are at higher level than most previous works. Circuit-level bus voltage scaling techniques, including [14, 20], which make the bus voltage and frequency track the bus traffic, or voltage swing reduction [17], would not be applicable due to interoperability reasons. Bus coding that minimizing the transition activities on buses [18, 15, 13, 9, 2] would not be applicable, either. Buses segmentation to age scaling techniques, including [14, 20], which make the bus constrained by the number of ports available on the physical inter-

## Table 1: Definitions

<table>
<thead>
<tr>
<th>string</th>
<th>tree</th>
</tr>
</thead>
<tbody>
<tr>
<td>a ( b ) ( c )</td>
<td><img src="" alt="Tree Diagram" /></td>
</tr>
<tr>
<td>b ( a ( c ) )</td>
<td><img src="" alt="Tree Diagram" /></td>
</tr>
<tr>
<td>a ( b ( c ) ( d ) ) ( e ( f ) )</td>
<td><img src="" alt="Tree Diagram" /></td>
</tr>
</tbody>
</table>

**Figure 1: Examples of tree strings.**

We require the transactions in the system are peer-to-peer. Multicast or broadcast transactions are not allowed.

**Definition 4 (Tree string)** A tree string is a string representation of a tree. It is obtained by in-order traversal of the tree. The root of the tree is traversed first, then recursively each child is traversed. For example, in Figure 1, the string a ( b ( c ) ) represents a tree of three nodes, with a the root node and b and c leaf nodes. A matched pair of parentheses with the substring inside represents a subtree. The string a ( b ( c ) ( d ) ) ( e ( f ) ) represents a tree of six nodes. c and d are two subtrees (also leaf nodes) of b, and b ( c ) ( d ) and e ( f ) are two subtrees of a.

**Definition 5 (Tree grammar)** Let Σ be an alphabet Σ = {u | u ∈ U} ∪ { ( , ) }, and a node u is denoted by a lower-case Roman letter. A tree is represented by a tree string t that can be generated from grammar G = (V, Σ, P, S), where V = {B, E} is a set of variables, S is a start symbol, P is a set of productions V → V ∪ Σ:

E → u
B → (E)
E → EB
S → E

and if a node u appears in t, it appears exactly once.

**Definition 6 (Tree language)** A language L(Σ) = {t | t is in Σ* and S ⇒ t} is a set of tree strings generated by grammar G. We also use L(v) = {t | t is in Σ* and v ⇒ t} to denote the set of strings generated with the start symbol v ∈ V, and L(v*) = {t* | t is in Σ* and v ⇒ t} to denote the set of strings that has zero or one or more concatenated substrings each of which is generated with a start symbol v ∈ V.

Let |t| represent the length of the tree string t. It is easy to see that for a tree containing n nodes, |t| = 3n − 2.

A tree topology can be represented by multiple tree strings. For example, a ( b ( c ) ) and b ( a ( c ) ) in Figure 1 represent the identical topology with different roots. Even with the same root, tree string a ( b ( c ) ) and a ( c ) ( b ) represent the same tree. Since any node (capable of bus management) on a FireWire bus can be the root, we can pick one node as the root and order the rest so that we are able to obtain a canonical form of a tree string.

3. PROBLEM FORMULATION

We generate tree topologies for FireWire by incrementally attaching new nodes to existing trees. We have developed a formal representation for modeling trees and generating tree topologies. In this section we give several definitions, followed by the cost function and our problem statement.

### 3.1 Definitions

**Definition 1 (Node u ∈ U)** A node u is a component in the system that has bus interfaces ready to connect to other components. p_u is the number of ports available for u. S_u is a finite set of speeds that node u can work at.

**Definition 2 (Tree)** A tree is a connected component C ⊆ U with exactly |C| − 1 undirected edges.

**Definition 3 (Transaction τ ∈ Γ)** A transaction τ = (u_1, u_2, s, w) is a data transfer behavior between two nodes u_1 and u_2 at the transfer speed s with non-zero workload w, where s ∈ S_u_1 ∩ S_u_2, and w is the amount of data (in byte) transferred.
Definition 7 (Transforming function $H$) A transforming function $H$ converts a tree string to its canonical form by the means of inorder traversal with sorting of labels. The canonical form of a tree string $t$ is: $\forall a \in t$, $a$ and its all children are sorted in a lexicographical order. Tree string $t_1=\{a \mid b(c,d)\} \mid e(f)\}$ in Figure 1 is in its canonical form. Tree string $t_2=\{a \mid e(f)\} \mid b(c,d)\}$ is not in its canonical form since $a$ and its children $e$ and $b$ are not sorted. Thus we have $t_1=H(t_2)$.

New trees can be formed by adding a node $x$ to an existing tree. The node can be either attached as a leaf node or inserted as a non-leaf node. We define a growing pattern $F(t,x)$ to help incrementally generate larger trees from smaller ones.

Definition 8 (Growing function $F$) $L(\Sigma \cup \{x\}) = L(\Sigma) \cdot F(t,x)$, for all $t \in L(\Sigma)$. Tree strings in $L(\Sigma \cup \{x\})$ can be derived from trees in $L(\Sigma)$ according to the following rules:

$$F(t,x) = \begin{cases} \delta(\{x\}) & \text{if } t = \delta, \\ \delta(\{x\} \mid \{\beta\}) \cup \delta(\{x\} \mid \{\gamma\}) & \text{if } t = \delta(\{\beta\}), \\ \delta(F(\{x\}) \mid \{\beta\}) \cup \delta(F(\{x\}) \mid \{\gamma\}) & \text{if } t = \delta(\{\beta\} \cup \{x\}), \\ 0 & \text{if } t = \delta(\{\beta\} \cup \{x\}), \\ \delta(F(\{x\}) \mid \{\beta\}) \cup \delta(F(\{x\}) \mid \{\gamma\}) & \text{if } t = \delta(\{\beta\} \cup \{x\}), \\ \delta(F(\{x\}) \mid \{\beta\} \cup \{x\}) & \text{if } t = \delta(\{\beta\} \cup \{x\}). \\ \end{cases}$$

(1)

where $\delta \in U$ represents the root of tree $t$, $\beta \in L(E)$ and $\gamma \in L(B^*)$.

Definition 9 (Tree string set $T$) A tree string set $T$ for a node set $U$ is a set of tree strings generated by grammar $G$ and:

1) $\forall t \in T, \forall a \in U, a \in t$,
2) $\forall t \in T, t.d = D_0, d_0 \in U, D \in L(B^*)$,
3) $\forall t_1, t_2 \in T, t_1 \neq t_2$, then $H(t_1) \neq H(t_2)$, and
4) for any tree string $t = d_0D,d_0 \in U, D \in L(B^*)$, $|t| = 3|U| - 2$, $\exists t' \in T, H(t) = H(t').$

In other words, each tree string in $T$ contains all nodes in $U$. All the tree strings have the same root node $d_0$. No two tree strings in $T$ have the same canonical representation. Tree set $T$ represents a complete set for all the tree topologies for the node set $U$.

We assume that all the nodes in $U$ are connected to form a single tree topology. A forest consisting of multiple trees is not allowed.

Lemma 1 (Tree generation) Given a tree string set $T$ for a node set $U$, a new tree string set $T'$ for the node set of $U \cup \{x\}$ is derived from $T$ without producing identical topologies by applying growing function $F$ to each tree in $T$: $T' = T \cdot F(t,x)$, for all $t \in T$.

Due to paper length limitation, the proof of Lemma 1 is omitted. Please refer to [16] for the details.

Definition 10 (Port count constraint) A tree can be represented in the form of $dB^k$, where $d$ represents the root of the tree, and $B^k(B \in L(B))$ represents all of its $k$ subtrees. The port count constraint is: $\forall u \in U$,

$$\begin{cases} p_u \geq k + 1 & \text{if } p \text{ is a non-root node,} \\ p_u \geq k & \text{if } p \text{ is the root node.} \\ \end{cases}$$

(3)

where $p_u$ is the port count for node $u$.

For example, in Figure 1, the tree string a (b (c) (d) ) (e (f) ) satisfies (3) if $f_a, f_e \geq 2$, $f_b \geq 3$ and $f_c, f_g, f_f \geq 1$.

Corollary 1 (Connectivity condition) Given a node set $U$ of $n$ nodes, a tree topology that connects all the nodes exists, iff

$$\sum_{u \in U} p_u \geq 2n - 2$$

(4)

where $p_u$ is the port count of node $u$.

A tree is legal if every node in $U$ satisfies the port count constraint (3) and connectivity condition (4).

3.2 Cost Function

Given a transaction $\tau = \{u_1, v_1, s_1, w_1\}$, all the nodes $u \in U$ can be categorized into three sets: $M_1, M_2,$ and $M_3$. $M_1 = \{u_1, v_1\}$ consists of communicating nodes. $M_2$ consists of all the nodes that repeat the transaction $\tau$ on the routing path. $M_3$ consists of the nodes not involved in the transaction $\tau$. We say the working modes $m_{u_2}$ for the node $u$ in the above sets each are transferring, repeating, and idle, respectively.

For a given node $u$, the power function $P$ is a function of the port number $p_u$ and working mode $m_u$, denoted as $P(p_u, m_u)$. Power function can be a lookup table whose data entries come from manufacturer’s data sheets [19].

We define the power function of a transaction $\tau$ and a tree $t$ as:

$$P(\tau,t) = \sum_{u \in M_1} P(p_u, m_u) + \sum_{u \in M_2} P(p_u, m_u) + \sum_{u \in M_3} P(p_u, m_u)$$

(5)

Power function $P(\tau,t)$ represents the total bus power of the whole systems during the transaction $\tau$. It consists of power of nodes involved in the transaction (both transferring and repeating node) and power of idle nodes.

For a transaction $\tau$, Effective transaction time is defined as:

$$D_\tau = \frac{w}{s}$$

(6)

where $w$ is the workload and $s$ is the transmission speed.

Note that effective transaction time may not be equal to the actual time to complete a transaction. Consider two transactions start at the same time, both transferring data at the same speed and both taking one minute to complete. Assume they equally share the total bandwidth, the effective transaction time for each transaction is only half a minute.

During a given time period $D$, we suppose there are $k$ transaction instances $\{\tau_i\}$ ($i = 1, \ldots, k$). The total effective transaction time is:

$$D_\tau = \sum_{i=1}^{k} D_{\tau_i}$$

(7)

We define utilization of the transaction $\tau$ is:

$$\lambda_\tau = \frac{D_\tau}{D}$$

(8)

Finally for a given tree string $t$, we define our cost function as:

$$C = \sum_{\tau \in T} P(\tau,t) \lambda_\tau$$

(9)

Cost $C$ represents the average energy consumption on the bus in unit time. However it does not include the energy consumption when the bus is completely idle (no transaction occurs).
3.3 Problem Statement

Given a tree \( t \) and a set of transactions \( \Gamma \), the tree is a feasible one if it satisfies the speed constraint:

\[
\forall \tau(ut, v, c, st, wt) \in \Gamma \text{ and } \forall x \in M_t, s_t \in S_c.
\]

That is, for a transaction, all the intermediate nodes on a routing path should support the transfer speed. We aim to find trees that has the minimum cost defined by (9). The input to the problem is a set of nodes \( V \) and a set of transaction \( \Gamma \). The output of the problem is a tree (or a set of trees) with minimum cost.

In case the input node set \( U \) does not satisfies the connectivity condition (4), we add hubs to interconnect the nodes so that the connectivity condition is satisfied. A hub is a special node that can repeat transactions but cannot be a peer node in a transaction. Several types of hubs are available, which differentiate by their port counts. The more ports a hub has, the more power it consumes when repeating packets. We are also interested in finding out which hub type is energy-optimal in connecting the node devices.

4. ALGORITHM

Tree topologies are incrementally generated using our grammar-based growing function. In this section we present the tree generation algorithm and the top level search algorithm. A brief discussion on complexity shows that asymptotically our algorithm generates much fewer trees than exhaustive approach and in practice, our technique produces even much fewer trees by applying system-level constraints.

4.1 Approach

We take an incremental approach to obtain the tree set of \( k+1 \) nodes from a tree set of \( k \) nodes. We use our growing function \( F \) to add a node to an existing tree either as a leaf node or as a non-leaf node. At each incremental step, if a tree topology fails to satisfy the port count constraint or the transfer speed constraint, it will not be included into the tree set. After obtaining a tree set for all the nodes, we calculate cost for each tree to search for optimal topologies.

4.2 Algorithms

AddAsLeaf\( (t, x, \Theta) \)

MinTree\( (V, \Theta, H) \)
The tree generation algorithm is shown in Figure 2. The inputs to the algorithm are a node set $U$ and hub type $h$. The output of the algorithm is a tree set containing all the feasible trees. In the pre-process procedure in Line 3, we check whether the node set $U$ satisfies the connectivity condition (4). If port count is not enough, we add adequate number of hubs of type $h$ into the node set, thus forming a new node set $U'$. We also sort the nodes in $U'$ by their port counts to facilitate the tree generation described below. Array $p[n]$ (Line 4) keeps the port count information of all nodes in $U'$ during the process of tree generation. Line 5-6 gets the first node in $U'$ and initialize the tree set $T$. The while loop (line 7-18) incrementally generates new trees and expands tree set. Two main steps are $AddAsLeaf()$ and $AddAsBranch()$ which add a new node to the existing tree set as a leaf node and as a non-leaf node, respectively.

Figure 3 shows the procedure $AddAsLeaf()$. When adding a new node $x$ to an existing tree $t$ as a leaf node, a connection between a node $u$ and one of its subtrees is identified. $x$ is inserted as the child of $u$ while the subtree as the child of $x$. Thus $x$ becomes a non-leaf node. We repeat this for all the subtrees of $u$. The procedure $AddAsBranch()$ is implemented similarly to $AddAsLeaf()$ as string manipulation and it not shown.

A top level algorithm is shown in Figure 4. We assume the connectivity condition (4) is not satisfied thus we try different types of hubs in the outmost loop (line 5 and 15). Otherwise the loop can be safely removed. Line 6 generates all feasible trees and stores them in set $T$. In the loop of line 11-19, we check to see whether the speed constraint is satisfied. If yes, we then calculate its cost and save it if it is minimum cost. Finally we output the optimal tree set with hub type or no solution message if all topologies fails to satisfy the constraints.

### 4.3 Complexity

Let us first see the complexity of an exhaustive approach. Given a node set $U$ of $n$ nodes, we can permute the nodes and obtain $n!$ strings, each consisting of $n$ nodes. For each string, we need to add $n-1$ pairs of parenthesis to form a tree string. For each parentheses pair, we have $n-1$ locations to add, and adding parentheses pairs is independent with each other. Thus we have $2^{n-1}$ of ways to add $n-1$ pairs of parentheses. Altogether, we can obtain $n!2^{n-1}$ trees from a node set of $n$ nodes. Note that among those trees, there are trees that topologically identical but differ in root nodes, trees that are not in their canonical forms, and trees that do not satisfy constraints.

Our algorithm assumes a node to be the root node and trees differ only in the root will not be repetitively generated. Our algorithm generates tree strings in their canonical forms and does not generated topologically identical tree strings.

In the $AddAsLeaf()$ routine, the if-branch (line 8 – 18) produces at most $k$ new strings ($k$ is the number of nodes in current tree $t$). $AddAsBranch()$ routine produces at most $(k-1)$ new strings. Thus we obtains at most $(2k-1)$ tree strings for a tree of $(k+1)$ nodes. Theoretically, our algorithm may produce at most $(2n-3)!!$ patterns for a node set of size $n$, which sets a very loose upper bound of generated tree strings. This is already asymptotically smaller than the exhaustive approach. In reality, our algorithm generates much fewer trees since we apply constraints at each incremental step. This greatly reduces the generated trees in that step and avoids fast growing of trees in the succeeding steps. For example, when $n = 8$, theoretically the exhaustive approach produces $5160960$ trees and our algorithm may produce at most $135135$ strings, only $2.6\%$ of the former approach. But in reality we only generated as few as $90$ trees (see Section 5) thanks to the constraints we applied.

### 5. EXPERIMENTAL RESULTS

We apply our algorithm to two FireWire bus examples. We use Firebug [5], a software bus snooping tools, to monitor the bus traffic and obtain the workload information. In the first example we have eight nodes to be connected. The second example has the similar setup but in a larger scale, which makes it almost impossible for the exhaustive approach to find out a solution in a practical time period. Our algorithm generates optimal tree sets efficiently. Our experimental results show that the optimal solutions we found save up to $15\%$-$20\%$ energy compared to an arbitrarily generated topology. Furthermore, workload balanceness and hub types have perceivable influences on energy cost. We have built a web-based tool to facilitate the user to interact with the core algorithm on the server side.

<table>
<thead>
<tr>
<th>Port/node</th>
<th>Transfer</th>
<th>Repeat</th>
<th>Idle</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>158.4</td>
<td>138.6</td>
<td>125.3</td>
</tr>
<tr>
<td>2</td>
<td>234.3</td>
<td>217.7</td>
<td>174.7</td>
</tr>
<tr>
<td>3</td>
<td>379.5</td>
<td>320.1</td>
<td>247.5</td>
</tr>
<tr>
<td>4</td>
<td>676.5</td>
<td>498.3</td>
<td>412.5</td>
</tr>
<tr>
<td>6</td>
<td>924.0</td>
<td>673.2</td>
<td>541.2</td>
</tr>
</tbody>
</table>

Table 1: Power data of FireWire interface (in mW).

<table>
<thead>
<tr>
<th>Device</th>
<th>Max speed(Mbps)</th>
<th>port #</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mac1</td>
<td>400</td>
<td>2</td>
</tr>
<tr>
<td>Mac2</td>
<td>400</td>
<td>2</td>
</tr>
<tr>
<td>PC1</td>
<td>400</td>
<td>2</td>
</tr>
<tr>
<td>HD1</td>
<td>200</td>
<td>2</td>
</tr>
<tr>
<td>Cam</td>
<td>100</td>
<td>1</td>
</tr>
<tr>
<td>iBot1</td>
<td>200</td>
<td>1</td>
</tr>
<tr>
<td>iBot2</td>
<td>200</td>
<td>1</td>
</tr>
<tr>
<td>Hub</td>
<td>400</td>
<td>3/4/6</td>
</tr>
</tbody>
</table>

Table 2: A list of FireWire devices

<table>
<thead>
<tr>
<th>Trans.</th>
<th>u1</th>
<th>u2</th>
<th>Speed(Mb/s)</th>
<th>Workload (x1000Mb)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Mac1</td>
<td>HD1</td>
<td>200</td>
<td>13</td>
</tr>
<tr>
<td>2</td>
<td>Mac1</td>
<td>PC1</td>
<td>400</td>
<td>25</td>
</tr>
<tr>
<td>3</td>
<td>Mac1</td>
<td>Cam</td>
<td>100</td>
<td>80</td>
</tr>
<tr>
<td>4</td>
<td>Mac1</td>
<td>iBot1</td>
<td>200</td>
<td>46</td>
</tr>
<tr>
<td>5</td>
<td>Mac2</td>
<td>HD1</td>
<td>200</td>
<td>5</td>
</tr>
<tr>
<td>6</td>
<td>PC1</td>
<td>iBot2</td>
<td>200</td>
<td>46</td>
</tr>
</tbody>
</table>

Table 3: A list of transactions
Example I

We have seven devices to be connected with FireWire bus interfaces, as listed in Table 2: Mac1 and Mac2 are two desktop Mac computers, PC1 is a notebook computer, HD1 is a FireWire hard drives, Cam is a Camcorder, iBot1 and iBot2 are two web cameras. A hub is added to the device list in order to satisfy the connectivity condition.

We use FireBug to capture the workload information. FireBug can keep track of all the activity on the FireWire bus and report to the user the desired events by filtering out the irrelevant ones. We first arbitrarily interconnect all the devices and turn on FireBug to monitor and record the traffic on the bus, and extract transaction-related information from FireBug log file. For example, we obtain the nodes involved in a transaction, the data transfer speed and the number of packets transferred. Then we obtain the transaction table shown in Table 3.

Table 4 shows the experiment results. Although this experiment looks simple, to find the optimal solution is not trivial. Exhaustive enumeration will produce 5160960 trees (see the last section). Our algorithm shrinks the tree set sizes down to less than 400 (first line of Table 4) using our grammar-driven tree generation.

MaxCost and MinCost are the maximum and minimum cost value for all generated feasible trees. In three cases ($f_n = 3, 4, 6$), the differences between MinCost and MaxCost are ranging from 12.2% to 16.6%, representing the potential energy savings by selecting the trees with MinCost. It is interesting to see that the more ports the hub has, the more energy the tree consumes. The reason is that the hub with more ports consumes more energy to repeat packets. Therefore for this example, a three-port hub is the optimal solution.

Figure 7, 5 and 6 show the optimal tree sets when using hubs of three, four and six ports, respectively. When using a three-port hub, four trees are found (see Figure 7). When using a six-port hub, only four ports of the hub are used. This is because for some transactions, it costs less when the two peer nodes are directly connected (if possible) instead of going through a hub. Trees in Figure 5 and Figure 6 are different even in both cases four ports are used. The reason is that different hub types in the two cases causes different energy consumption.

In order to see whether the potential energy savings are sensitive to the workload balanceness, we change the workload on transaction 3 (between Mac1 and Cam) and generate optimal topology for each workload value. Transaction 3 originally has the largest workload among all transactions. We change its workload value from the average of all transactions to positive infinity (disabling all other transaction). Figure 8 shows the curve of the workload percentage vs. the potential energy savings. The workload percentage is the ratio between the workload of the selected transaction to the total workload of all transactions. The curve shows that the higher the
our technique can be applied to more applications and will show other tree-like architectures. As low power serial buses becomes orthogonal to our techniques. It is foreseeable that additive energy savings can possibly be achieved. Hub type selection influences the optimal solution points due to variations in their individual power behavior.

Example II
We use three Mac computers, four FireWire hard drives, one printer, one scanner and one camcorder, totally ten devices. To satisfy the connectivity condition, we add three, two, and one hub when using three-port, four-port, six-port hubs, respectively (first two rows in Table 5). For the exhaustive approach, the problem of up to thirteen nodes becomes intractable in practice. Out algorithm generated highly compact tree sets (Row 3 of Table 5). Potential energy savings ranges 10.1%-14.2%.

Note that in our cost function, we only consider the time periods when there is traffic on bus. When the bus is complete idle, part or all the bus nodes can be potentially disabled, resulting more energy savings. In the implementation of FireWire bus drivers, the link layer and above layers can be disabled for low power if no transaction is on the node. While in our examples, we assume all the layers are on all the time. Even for the physical layer controller, dynamic power management techniques can be applied to disable it when there is no traffic passing through it. All the above conditions are orthogonal to our techniques. It is foreseeable that additive energy saving could be achieved by combining our technique together with other power management techniques.

Table 5: The number of devices with different hub types.

<table>
<thead>
<tr>
<th>hub type</th>
<th># of hub</th>
<th># of trees</th>
<th>MaxCost</th>
<th>MinCost</th>
<th>diff(%)</th>
<th># of optimal trees</th>
</tr>
</thead>
<tbody>
<tr>
<td>p = 3</td>
<td>3</td>
<td>13</td>
<td>45761</td>
<td>332.8</td>
<td>10.1</td>
<td>3</td>
</tr>
<tr>
<td>p = 4</td>
<td>2</td>
<td>12</td>
<td>17001</td>
<td>304.4</td>
<td>13.3</td>
<td>2</td>
</tr>
<tr>
<td>p = 6</td>
<td>1</td>
<td>11</td>
<td>2013</td>
<td>270.4</td>
<td>14.2</td>
<td>1</td>
</tr>
</tbody>
</table>

6. CONCLUSION
This paper presents a method for optimizing peer-to-peer serial bus topology for energy reduction. We use a canonical string form to represent trees that is both concise and easy to manipulate. We propose an incremental approach to enumerate tree topologies. By applying a number of constraints in each enumeration step, we are able to obtain both complete and compact tree sets without producing redundant trees. We capture the bus workload information by monitoring the bus activity and factor it into the cost function for energy optimization.

Workload balanceness has an impact on the potential energy savings. The more imbalanced the workload is, the more potential energy savings can possibly be achieved. Hub type selection influences the optimal solution points due to variations in their individual power behavior.

Although we use FireWire bus to demonstrate the effectiveness of our technique, our approach is general enough to apply to many other tree-like architectures. As low power serial buses becomes more popular in computer systems and system-on-chip, we believe our technique can be applied to more applications and will show significant energy savings.

Current topology optimization is static, requiring the bus to reconfigure at least once to form an optimal topology. It is possible to construct a bus topology with redundant physical links while dynamically configure it to form logic tree topologies for performance, energy-saving, and fault-tolerance reasons.

7. REFERENCES